

ABSTRACT

The invention provides a bandgap voltage reference circuit that is adapted to provide a ΔV_{be} of sufficiently large value that no amplification is required, and therefore any offset contribution is not gained. Using a stacked arrangement of
5 three pairs of transistors, the invention reduces the requirement for multiple resistors within a circuit and can therefore minimise errors due to resistor matching and value. Internally provided circuitry for reducing voltage curvature is provided with the result that a circuit having low offset sensitivity and curvature correction is provided.